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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BLANER et al.

Serial No. : 09/898,812

Patent No.: 6,857,065 B2

Issued: Feb. 15, 2005

For: System and Method for Initializing
a Data Processing System by
Selecting Parameters...and a
Parallel non-Volatile Memory

ATTY. DOCKET NO.: BUR920000183US1

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Examiner: E. Chang

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Applicant respectfully submits the enclosed proposed Certificate of Correction (in duplicate), which corrects clerical errors made by the U.S. Patent and Trademark Office in Letters Patent No. US 6,857,065 B2, issued February 15, 2005.

Respectfully submitted,

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ATTORNEY FOR APPLICANTS

CERTIFICATE OF MAILING
37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First-Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date below.

4/12/05
Date

Signature

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,857,065 B2

DATED : Feb. 15, 2005

INVENTOR(S) : BLANER et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At (54) of the Title Page, please delete the entire Title and insert therefore the following:

System and Method for Initializing a Data Processing System by
Selecting Parameters from One of a User-Defined Input, a Serial
Non-Volatile Memory and a Parallel Non-Volatile Memory

At the top of Col. 1, please delete the entire Title and insert therefore the following:

System and Method for Initializing a Data Processing System by
Selecting Parameters from One of a User-Defined Input, a Serial
Non-Volatile Memory and a Parallel Non-Volatile Memory

In Col. 6 (Claim 9), please delete lines 50-55 and insert therefore the following:

Utilizing said preferred set of initialization data stored in said plurality
of parameter registers to initialize said data processing system
from an uninitialized state to a minimal level of operation.

MAILING ADDRESS OF SENDER:

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PATENT NO. **US 6,857,065 B2**

No. of additional copies

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